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10/023861



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U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10023861	12/21/2001	438	6a4	1765	Tran, B

\*\*APPLICANTS: Kwon Jae;

\*\*CONTINUING DATA VERIFIED:  
None BT

\*\* FOREIGN APPLICATIONS VERIFIED:  
REPUBLIC OF KOREA 2001-60631 09/28/2001  
BT

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no  
35 USC 119 conditions met ☐ yes ☒ no  
Verified and Acknowledged Examiners's initials BT

ATTORNEY DOCKET NO

10729-P6746US0

TITLE : Method of semiconductor device isolation

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>Application Examiner</b>	
		<b>PREPARED FOR ISSUE</b>	
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